

1001 U.S. PTO
10/055991



U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10055991	FILING DATE 01/28/2002	CLASS 257	SUBCLASS	GAU 2811	EXAMINER <i>Gebremaman</i>
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**APPLICANTS: Miwa Hiroyuki;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A DIV OF 08/932,832-09/18/1997 08/532,057

** FOREIGN APPLICATIONS VERIFIED:

JAPAN P06-229612 09/26/1994

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials		ATTORNEY DOCKET NO SON-1351/DIV
TITLE : Method of making a bipolar transistor having a reduced base transit time		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner Primary Examiner		Total Claims	Print Claim for 0.6
ISSUE FEE Amount Due Date Paid		DRAWING	
<input type="checkbox"/> TERMINAL DISCLAIMER		Sheets Drawn	Fig. Drawn Print Fig.
PREPARED FOR ISSUE		Application Examiner	
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